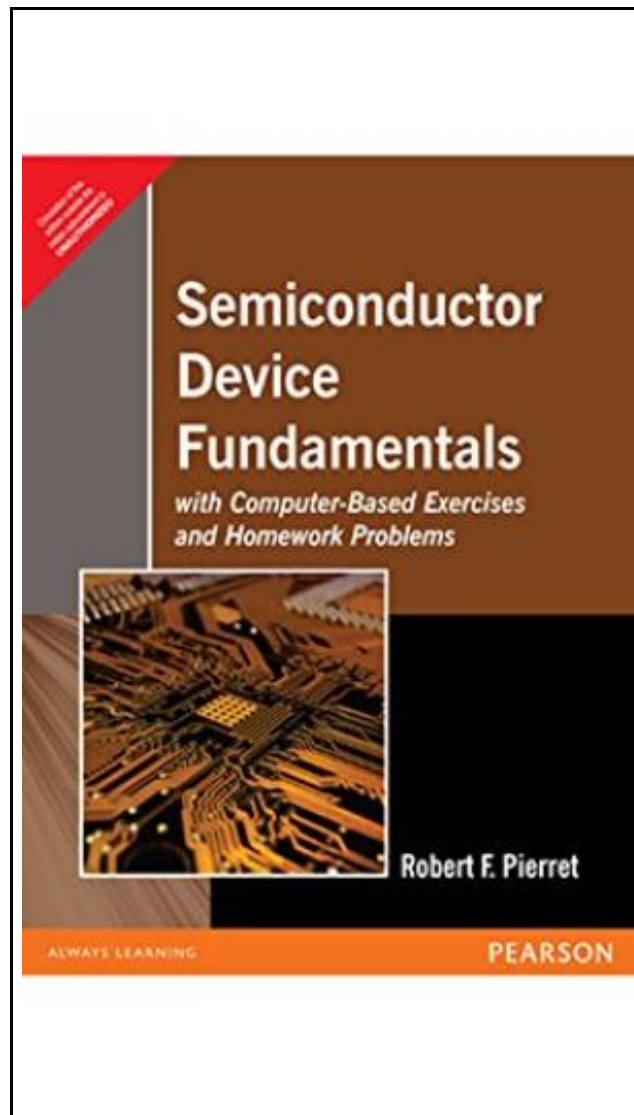


Semiconductor Device Fundamentals with Computer-Based Exercises and Homework Problems



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Reviews

This publication is definitely not simple to begin on studying but really exciting to read. It is actually rally fascinating throgh reading time. Your life span will be enhance when you complete looking at this publication.

(Laurence Littell)

SEMICONDUCTOR DEVICE FUNDAMENTALS WITH COMPUTER-BASED EXERCISES AND HOMEWORK PROBLEMS



Pearson Education, 2006. Softcover. Book Condition: New. First edition. 18 x 24 cm. Contents: PART I: SEMICONDUCTOR FUNDAMENTALS. 1. Semiconductors -- A General Introduction. 2. Carrier Modeling. 3. Carrier Action. 4. Basics of Device Fabrication. R1. Part I Supplement and Review. PART II: A. PN JUNCTION DIODES. 5. PN Junction Electrostatics. 6. PN Junction Diode -- I-V Characteristics. 7. PN Junction Diode -- Small-Signal Admittance. 8. PN Junction Diode -- Transient Response. 9. Optoelectronic Diodes. PART III: BJTS AND OTHER JUNCTION DEVICES. 10. BJT Fundamentals. 11. BJT Static Characteristics. 12. BJT Dynamic Response Modeling. 13. PNP Devices. 14. MS Contacts and Schottky Diodes. R2. Part II Supplement and Review. PART IV: FIELD EFFECT DEVICES. 15. Field Effect Introduction -- the J-FET and MESFET. 16. MOS Fundamentals. 17. MOSFETs -- The Essentials. 18. Nonideal MOS. 19. Modern FET Structures. R3. Part III Supplement and Review. Salient Features * The computer-based exercises and homework problems permit the replacement of plug-and-chug boredom with more challenging realistic problems * Other special features include tables that summarize the required reading, difficulty level, and suggested weighting of end-of-chapter problems, ?crib-sheet? ?like equation summaries in the critical beginning chapters, and direct reproductions of device data and computer-generated plots. Printed Pages: 816.



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